

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

3/a  
Leuris  
9-1-99

In re application of

HIEP V. TRAN

Serial No. 08/938,163 (TI-19819)

Filed September 26, 1997



For: SELF SELECT PRECHARGED DOMINO LOGIC CIRCUIT

Art Unit 2819

Examiner Don Le

RECEIVED

Assistant Commissioner for Patents  
Washington, D. C. 20231

AUG 30 1999

TECHNOLOGY CENTER 2800

Sir:

**AMENDMENT UNDER 37 C.F.R. 1.115**

In response to the Office action dated May 27, 1999, please amend the above identified application as follows:

**In the claims:**

09/07/1999 JMC/MILLA 00000019 200668 08938163

01 FC:102 390.00 Amend claim 1 as follows:  
02 FC:103 162.00 CH

1. (Amended) A logic circuit which comprises:

- (a) a precharge node for retaining one of a high signal state and a relatively low signal;
- (b) an input terminal;

(c) a first transistor of one of n-channel or p-channel type having a control electrode and a current path coupled between a source of power and said precharge [terminal] node;